

IN THE UNITED STATES PATENT OFFICE

Applicant: Austin H. Lesea  
Assignee: Xilinx, Inc.  
Title: "Memory Cells Enhanced for Resistance to Single Event Upset"  
Serial No.: Not Yet Known      File Date: 02-26-04  
Examiner: Not Yet Known      Art Unit: Not Yet Known  
Divisional of  
Serial No.: 10/125,666      File Date: 04-17-02  
Docket No.: X-1026-1D US

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Mail Stop PATENT APPLICATION  
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Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

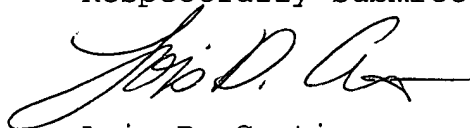
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the twelve (12) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 10/125,666 filed April 17, 2002 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,



Lois D. Cartier  
Agent for Applicant  
Reg. No. 40,941

